

International Semester in Telecommunications Engineering 2024/2025

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Courses taught in English in 2024/2025

Area	Acronym	Name	ECTS	Semester ¹	UVa course code
Electronics	DE	Digital Electronics	6	1	48072
Signal Theory & Communications	FML	Fundamentals of Machine Learning	6	1	75097
	ICTA	Information and communications technology in automotive industry	6	1	46675
	WTS	Wireless Telecommunication Systems	6	2	45045
	ISP	Introduction to Signal Processing	6	2	75105
Mathematics	AM	Advanced Mathematics	6	2	48068
	NA	Numerical Algorithms	6	1	45032
Economy	ECO	Introduction to business economics and administration	6	1	45010
Signal Theory, Communications, Telematic Engineering and Electronics	PROJECT ²	Bachelor's degree final project	6	1 or 2	45036
		Bachelor's degree final project	12	1 or 2	46683 or 46684 or 46680
		Internship	6	1 or 2	45035
		Master's degree final project	12	1 or 2	53817

¹ 1: Autumn (lectures from September 10 to December 20, 2024; exams from January 8 to February 7, 2025)

2: Spring (lectures from February 10 to May 29, 2025; exams from June 2 to June 30)

² PROJECT: students can choose one final project with or without internship, using the appropriate course codes, to make a combination 6, 12 or 18 ECTS.

Project

Students of the International Semester can do their degree's final project with us. This can imply taking two UVA courses: one of the available bachelor's or master's final project (course codes and different effort in ECTS vary among the programs we offer), optionally complemented by an internship in the research group to make a deeper final project. The project can be taken in any of the two semesters and it is also possible to take it during the full year.

Students will have to develop a project in the area of Telecommunications. Two departments will offer projects to international students: Signal Theory, Communications and Telematic Engineering, and Electronics.

When a student wants to take a project, she/he must contact the ETSIT International Coordinator (IC) (subdireccion.relaciones.tel@uva.es) and send him both a Curriculum Vitae and a list of areas of interest. If the CRI considers that she/he is eligible, the IC will distribute the CV between the different research groups working in the areas suggested by the students. Research groups interested in the students will contact her/him directly and offer different topics for the project. When the student reaches an agreement with the research group, she/he will contact the IC and the research group to confirm the agreement. **This process has to be completed before signing any learning agreement.**

Maximum number of students in each course

The maximum number of admitted student in each course is 20.

English level for students

All students are required a B2 level of English.

Location

Classes will be planned in ETSITs rooms (classroom and laboratories). The project will be taken in the premises of each research group.

Class schedule

Autumn Semester

	Monday	Tuesday	Wednesday	Thursday	Friday
9h	FML A107	FML A107	ICTA 1L013/1L014	DE A107	
10h	FML A107	FML A107	ICTA 1L013/1L014	DE A107	
11h	NA A107/2L001	DE A107/1L020	ICTA 1L013/1L014	NA A107	
12h	NA A107/2L001	DE A107/L020	ICTA 1L013/1L014	NA A107	
13h					
14h					
15h					
16h			ECO A107	ECO A107	
17h			ECO A107	ECO A107	
18h					
19h					

Spring Semester

	Monday	Tuesday	Wednesday	Thursday	Friday
9h			ISP A108	ISP 2L003	
10h			ISP A108	ISP 2L003	
11h			AM A107	AM A107	
12h			AM A107	AM A107	
13h					
14h					
15h					
16h			WTS A107		
17h			WTS A107		
18h					WTS 2L004-5
19h					WST 2L004-5

Exam schedule

Students have two opportunities to pass the courses. The first (“ordinary” call) will be either a final exam or a set of intermediate exams or reports during the course. The second (“resit”) will always be a final exam or report and will only apply if a student fails in the first call.

Autumn Semester

Course	Ordinary call	Resit
DE	Jan 13, morning & afternoon	Jan 30, morning & afternoon
FML	Interim reports	Final report
NA	Jan 8, afternoon	Feb 9, afternoon
ECO	Jan 9, morning	Feb 27, morning
ICTA	Jan 15, morning	Feb 3, morning

Spring Semester

Course	Ordinary call	Resit
AM	Jun 12, morning	Jun 30, morning
WTS	Jun 13, afternoon	Jun 27, afternoon
ISP	Jun 4, afternoon	Jun 18, afternoon

Contact

ETSIT International Coordinator (CRI) is Eduardo Gómez Sánchez:
subdireccion.relaciones.tel@uva.es

Courses Syllabus:

Digital Electronics (DE)			
Code number:	48072	Number of ECTS:	6 ECTS
Semester:	Autumn	Language:	English
Lecturer(s) and contact:			
<ul style="list-style-type: none"> Dr. Luis Alberto Marqués Cuesta (lmarques@ele.uva.es) 			
Learning goals:			
At the end of the course, the student must be able to:			
<ul style="list-style-type: none"> Know and understand fundamental concepts related to digital electronic circuits. Analyze and design (synthesize) basic digital electronic circuits from logic gates, combinational and/or sequential modules. Choose, among the different types of mass storage systems, those that fit a specific application. Use CAD tools based on hardware description languages for the design of digital blocks and the subsequent verification of their correct operation. Organize, plan and manage laboratory time. Communicate, both in writing and orally, the procedure used in the laboratory and the difficulties that may arise. 			
Contents:			
UNIT 1 – FUNDAMENTALS			
1.1.- Introduction.			
1.2.- Boolean Algebra.			
1.3.- Two-variable logic functions. Functional completeness.			
1.4.- Information coding.			
1.5.- Minimization of logic functions. Canonical form.			
UNIT 2 – COMBINATIONAL CIRCUITS			
2.1.- Introduction.			
2.2.- AND-OR design and analysis.			
2.3.- NAND-NOR design and analysis.			
2.4.- Hazards.			
<u>Lab session 1</u> – Structural design (1st part).			
UNIT 3 – COMBINATIONAL MODULES			
3.1.- Introduction.			
3.2.- Decoder.			
3.3.- Encoder.			
3.4.- Code converter.			
3.5.- Multiplexer.			
3.6.- Demultiplexer.			
3.7.- Comparator.			
3.8.- Adder.			
3.9.- Arithmetic-Logic Unit (ALU).			
<u>Lab session 2</u> – Structural design (2nd part).			
<u>Lab session 3</u> – RTL design.			
UNIT 4 – LATCHES AND FLIP-FLOPS			
4.1.- Introduction.			
4.2.- Static latches.			

4.3.- Dynamic latches.

4.4.- Flip-flops.

UNIT 5 – SEQUENTIAL CIRCUITS

5.1.- Introduction.

5.2.- Design procedure.

5.3.- Moore and Mealy automata.

Lab session 4 – Algorithm-based description (1st part).

UNIT 6 – SEQUENTIAL MODULES

6.1.- Introduction.

6.2.- Storage registers.

6.3.- Transferring digital information. Buses.

6.4.- Counters.

6.5.- Shift registers.

6.6.- Operational registers.

Lab session 5 – Algorithm-based description (2nd part).

UNIT 7 – MEMORIES

7.1.- Introduction.

7.2.- Random access memories.

7.3.- Sequential memories.

Prerequisites:

None.

Assessment:

Midterm exam (units 1-3) and final exam (units 4-7) account for 60% of the grade. Another practical, laboratory exam accounts for 40% of the grade.

Fundamentals of Machine Learning (FML)			
Code number:	75097	Number of ECTS:	6 ECTS
Semester:	Autumn	Language:	English
<p>Lecturer(s) and contact:</p> <ul style="list-style-type: none"> • Dr. Miguel Ángel Martín Fernández (migmar@tel.uva.es) • Dr. Lara del Val Puente (lara.val@uva.es) 			
<p>Learning goals:</p> <p>At the end of the course the student must be able to:</p> <ul style="list-style-type: none"> • Explain what machine learning is and enumerate the type of machine learning types. • Describe the basic theory of machine learning and its practical implications in system design. • Describe and apply various models of supervised and unsupervised machine learning. • Describe and apply regularization, validation and aggregation techniques in the development of systems based on machine learning. • Implement systems based on machine learning using Python. 			
<p>Contents:</p> <p>LESSON 0: Presentation and Introduction to Python</p> <p>LESSON 1: Introduction to machine learning</p> <p>LESSON 2: Is it feasible to learn? (First part)</p> <p>LESSON 3: The linear model: Classification and linear regression</p> <p>LESSON 4: Is it feasible to learn? (Second part)</p> <p>LESSON 5: The linear model: Logistic regression</p> <p>LESSON 6: Regularization</p> <p>LESSON 7: Validation</p> <p>LESSON 8: Neural networks</p> <p>LESSON 9: Support vector machines (SVM)</p> <p>LESSON 10: Decision trees</p> <p>LESSON 11: Some aspects to take into account in the design of supervised learning systems</p> <p>LESSON 12: Clustering</p> <p>LESSON 13: Dimensionality reduction</p> <p>LESSON 14: Deep Learning</p>			



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Prerequisites:

Good knowledge in maths and basic programming skills. Students will need to bring their own laptop.

Assessment:

Assessment Instrument:

- 90% Lab exercises (minimum: 50%).
- 10% Attitude and participation in training activities (minimum: 50%).

Resit:

- The marks of attitude and participation in training activities of the ordinary call will be kept.

Information and communications technology in automotive industry (ICTA)			
Code number:	46675	Number of ECTS:	6 ECTS
Semester:	Autumn	Language:	English
Lecturer(s) and contact: <ul style="list-style-type: none"> • Dr. Juan Carlos Aguado Manzano (jaguado@tel.uva.es) • Dr. Ignacio de Miguel Jiménez (Ignacio.miguel@tel.uva.es) 			
Learning goals: At the end of this sections, the student should be able to: <ul style="list-style-type: none"> • Use software tools for the analysis and design of commercial devices and ICT (Information and Communication Technologies) applications in vehicles. • Analyze and decode traces of basic protocols in vehicles. • Enumerate and describe the most important parameters of the physical layer of the basic protocols in vehicles. • Enumerate and describe ICT applications and basic services in vehicles. • Enumerate and describe basic elements of communications in intra-vehicular, inter-vehicular and vehicle to infrastructure communication networks. • Design and program applications and devices for intra-vehicular communications. • Use the documentation from OEM to develop and analyze ICT devices and applications in vehicles. 			
Contents: <ol style="list-style-type: none"> 1. Introduction to Vehicle Telematics. 2. Intra-Vehicular communications. CAN Bus. 3. Introduction to CANoe. 4. Programming in CAPL. 5. CANoe advanced options for emulating whole systems 6. Intra-vehicular communications. Other standards. 7. Design of ECUs. 8. ECU diagnosis. 9. Dataloggers. <p>Lab:</p> <ol style="list-style-type: none"> 1. Physical layer of the CAN bus. 2. CAN analysis: IGN signals, TeleAid Info-Call and Volume Control. 3. CAN analysis: Airbag signals. 4. CAN analysis: Real car trace. 5. Sending CAN messages using CANoe. 6. CAPL Program. 7. Captur Electronic Architecture: Controlling Infotainment from CANoe 8. MOST Optical Bus Analyzer. 9. ECU simulation using CANister. Breathalyzer design and development. 10. Datalogger. Diagnostics. 			
Prerequisites: This is an intermediate course, intended for learners with a background in computer and electrical engineering. To succeed in this course, you should have the following knowledge prerequisites: <ul style="list-style-type: none"> • Intermediate programming experience, preferable in C. 			



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- Familiarity with protocols, communications networks and telematic services.
- Basic use of laboratory equipment, mainly Oscilloscopes.

Assessment:

Online tests (10%), attitude and reports of labs corresponding to topics 1-6 (40%), attitude and reports of labs corresponding to topics 7-9 (35%), final test (15%).

Wireless Telecommunication Systems (WTS)			
Code number:	45045	Number of ECTS:	6 ECTS
Semester:	Spring	Language:	English
Lecturer(s) and contact: <ul style="list-style-type: none"> • Dr. Ramón de la Rosa Steinz (ramros@tel.uva.es) • Dr. Alfonso Bahillo Martínez (alfonso.bahillo@uva.es) 			
Learning goals: At the end of this sections, the student should be able to: <ul style="list-style-type: none"> • Know the options to experiment in the field of the radio amateur operation. • Work with regulations related to the radio frequency spectrum management. • Work with specifications related to radio telecommunication systems. • Identify transmissions with spectrum analysis equipment. • Connect the basic parameters that characterise a radio frequency system. • Interpret the technology involved in the radio telecommunication systems. • Estimate the radio coverage in point-to-point systems. • Enumerate and describe the communication systems studied. • Identify the planning requirements in terms of time and resources to develop projects 			
Contents: <ol style="list-style-type: none"> 1. AN INTRODUCTION TO RADIO: Concept revision. Logarithmic units. The radio frequency spectrum. Radio amateur operation as a way to experiment. 2. ANTENNA SYSTEMS TECHNOLOGY: Review of characteristics and parameters defining the antennas. Antenna feeders. Antennas applied to communication systems. 3. RECEIVERS AND TRANSMITTERS: Receivers technology. Transmitters technology. Interpreting transceiver wiring diagrams. The evolution of the radio. Software defined radio (SDR). 4. RADIO BROADCASTING: Amplitude modulation (AM) radio broadcasting. Frequency modulation (FM) and FM-stereo radio broadcasting. Digital broadcasting: RDS y DAB. Modulating in DAB. OFDM. 5. RADIO LINKS AND SATELLITE COMMUNICATIONS: Introduction and satellite orbits. Parameters that influence the communication: the link budget. Types of satellites. Satellites and radio amateur operation. Related modulating schemas: FSK and PSK. Radio links. Coverage estimation with software. 6. CELLULAR TELECOMMUNICATIONS: Basic standards. Second generation (2G): GSM, GPRS and EDGE. Modulations related to 2G. MSK, GMSK. Third generation (3G) and subsequent generations. UMTS, LTE, 5G. Modulations related to 3G and subsequent generations. Spread spectrum. 7. SHORT-RANGE WIRELESS DATA COMMUNICATIONS: Bluetooth. IEEE 802.11 – ISO/IEC 8802-11 (Wi-Fi). Other technologies. 			
Prerequisites: It will be very helpful some basic knowledge about electronics to understand schemas, and ability to			



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understand the concept of electromagnetic waves and its location in the radio frequency spectrum. For the applied part of the subject, it will be helpful some basic knowledge of the laboratory of electronic instrumentation (oscilloscope, multimeter, function generator), reasonable manual skills and being resourceful to build small prototypes.

Assessment:

Final exam (60%), reports and demonstration of a laboratory project (30%), regular in-class activities (10%).

Numerical Algorithms (NA)			
Code number:	45032	Number of ECTS:	6 ECTS
Semester:	Autumn	Language:	English
Lecturer(s) and contact: <ul style="list-style-type: none"> • Dr. Eduardo Cuesta Montero (eduardo.cuesta@uva.es) 			
Learning goals: At the end of this sections, the student should be able to: <ul style="list-style-type: none"> • Understand limitations of analytical methods and the need for numerical algorithms. • Understand how computers represent numbers and how these impact mathematical computations on computers. • Understand how we describe errors and approximations that result from using computers to solve mathematical equations and approximate mathematical functions. • Learn how to solve a system of linear equations numerically using direct and iterative methods. • Learn how to solve least-squares problems. • Understand how to approximate the functions using interpolating polynomials. • Learn how to solve definite integrals and initial value problems numerically. • Learn the application of the FFT . • Know how to solve complex differential problems. • Demonstrate the applications of numerical techniques to simple problems drawn from telecommunications and electronic engineering fields. 			
Contents: <ol style="list-style-type: none"> 1. PYTHON programming. 2. Direct methods for solving of linear systems. 3. Least squares approximation. 4. Iteration: linear and nonlinear. 5. The matrix eigenvalue problem. 6. Lagrangian interpolation. 7. Numerical integration and differentiation. 8. Trigonometric interpolation. 9. Numerical solution to ordinary differential equations. 10. Numerical solution to partial differential equations. 			
Prerequisites: Some background on linear algebra and calculus is strongly recommended.			
Assessment: Public presentation and discussion of assignments proposed throughout the course.			

Advanced Mathematics (AM)			
Code number:	48068	Number of ECTS:	6 ECTS
Semester:	Spring	Language:	English
Lecturer(s) and contact:			
<ul style="list-style-type: none"> Dr. Eduardo Cuesta Montero (eduardo.cuesta@uva.es) 			
Learning goals:			
<p>At the end of this sections, the student should be able to:</p> <ul style="list-style-type: none"> Manage problems involving complex variable and vector calculus, differential geometry, and differential equations. Solve analytically the most common ordinary and partial differential equations in engineering . Model mathematically a wide range of problems arisen in the degree. Numerically solve some common theoretical problems arisen in engineering. Discover the relationship between the subjects of the present course and other subjects, in fact the ones related to Telecommunication and Electronic Engineering. Use recommended bibliography to assess ideas and results. Understand further mathematical models related to Telecommunication and Electronic Engineering. 			
Contents:			
<ol style="list-style-type: none"> PARAMETRIC CURVES AND COMPLEX VARIABLE: Parametric curves, elementary complex functions, complex derivation and integration. Applications in practical instances. FOURIER ANALYSIS: Fourier series, Fourier transform, and discrete Fourier transform. Applications in signal processing. POWER SERIES AND LAPLACE TRANSFORM: Power series, Laurent series, Z-transform, and Laplace transform. Applications in the study of linear systems. ORDINARY DIFFERENTIAL EQUATIONS: Ordinary differential equations (ODEs) of order one and two. Applications in electric and electronic circuits analysis. NUMERICAL METHODS FOR ORDINARY DIFFERENTIAL EQUATIONS: Explicit and implicit Euler method, and higher order methods. PARTIAL DIFFERENTIAL EQUATIONS: Separation of variable method, Fourier method, and nonhomogeneous problems. Applications in wave propagation and diffusion processes. NUMERICAL METHODS FOR PARTIAL DIFFERENTIAL EQUATIONS: Difference equations schemes, for 1- and 2-dimensional problems. 			
Prerequisites:			
Some background on linear algebra and calculus is strongly recommended.			



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Assessment:

Written exam for the theoretical part and laboratory assignments for the part related to numerical methods.

Introduction to business economics and administration (ECO)														
Code number:	45010	Number of ECTS:	6 ECTS											
Semester:	Autumn	Language:	English											
Lecturer(s) and contact: <ul style="list-style-type: none"> Dr. Guillermo Aleixandre Mendizábal (galeixandre@uva.es) Dr. Juan José Juste Carrión (juan.juste@uva.es) 														
Learning goals: At the end of this sections, the student should be able to: <ul style="list-style-type: none"> Apply the basic principles of the economy and the company to the telecommunications sector. Identify the different types of companies, market structures, being able to calculate prices and quantities of equilibrium in each one of them. Distinguish the types of costs of the companies and their sources of financing. Interpret the economic, legal and institutional framework of the company. 														
Contents: <ol style="list-style-type: none"> Preliminary concepts in economy. The enterprise and the entrepreneur. Competitive markets in the short term: demand and supply. Production, costs, revenues and business benefits. Firms in the perfect competition market. Firms in non-competitive markets. Project appraisal decisions in the company. Financial statement analysis of the company and business financing. 														
Prerequisites: There are no academic preconditions to take this course.														
Assessment: <table border="1" data-bbox="226 1512 1406 1886"> <thead> <tr> <th>Assessment instruments</th> <th>Final grade percentage</th> <th>Observations</th> </tr> </thead> <tbody> <tr> <td>Resolution and delivery of in-class and out-of-class activities throughout the course.</td> <td>50%</td> <td>Each unit has in-class and out-of-class activities. All the activities done either in class or out of class are used in the assessment.</td> </tr> <tr> <td>Final exam (Questionnaire)</td> <td>25%</td> <td rowspan="2">It is necessary to score in the final exam equal to or higher than 5 points out of 10; It is necessary to score equal to or higher than 1,5 points out of 5 in each of the two parts (questionnaire and problems).</td> </tr> <tr> <td>Final exam (Problems)</td> <td>25%</td> </tr> </tbody> </table> <ul style="list-style-type: none"> Ordinary call: The final assessment of the course is the weighted sum of the different assessment instruments. Extraordinary call (the final exam will be repeated): The assessment is the best of these two options: <ul style="list-style-type: none"> Option 1: Considering the continuous assessment. Option 2: Without considering the continuous evaluation. The final exam will be 100% of the score. 				Assessment instruments	Final grade percentage	Observations	Resolution and delivery of in-class and out-of-class activities throughout the course.	50%	Each unit has in-class and out-of-class activities. All the activities done either in class or out of class are used in the assessment.	Final exam (Questionnaire)	25%	It is necessary to score in the final exam equal to or higher than 5 points out of 10; It is necessary to score equal to or higher than 1,5 points out of 5 in each of the two parts (questionnaire and problems).	Final exam (Problems)	25%
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Final exam (Problems)	25%													

Introduction to signal processing (ISP)			
Code number:	75105	Number of ECTS:	6 ECTS
Semester:	Spring	Language:	English
Lecturer(s) and contact: <ul style="list-style-type: none"> Dr. Tomasz Pieciak (tpieciak@tel.uva.es) 			
Learning goals: At the end of the course, the student will be able to: <ul style="list-style-type: none"> Differentiate signal types and their representations. Analyze signals in time, frequency and time-frequency domains. Design low- and high-pass digital filters and filter a digital signal (i.e. electrocardiogram). Characterize the self-affinity of a biomedical signal. Implement own basic digital signal procedures. 			
Contents: <ol style="list-style-type: none"> Introduction to signal processing, elementary algebra and mathematical analysis used in signal processing, continuous, discrete and digital signals. Signal representations, sampling and quantization, aliasing. Linear systems, convolution, correlation. Continuous and discrete-time Fourier transform, discrete cosine transform, sliding DFT, coherence, short-time Fourier transform, Fast Fourier Transform (Cooley-Tukey algorithm). Low- and high-pass filters, Finite and Infinite Impulse Response (FIR, IIR) filters design, filtering, parametric windows. Detrended fluctuation analysis (DFA). 			
Laboratory classes: <ol style="list-style-type: none"> Sinusoidal signal generation with specific parameters and noise distribution. Implementing own DFT and sliding DFT procedures. Implementing own low- and high-pass filter design procedures. Implementing own linear convolution procedure, filtering electrocardiogram signal. Implementing own short-time Fourier transform with an application to biomedical signals. Implementing own FFT procedure (Cooley-Tukey algorithm). Implementing own DFA procedure with an application to heart rate analysis. 			
Prerequisites: Basic knowledge in algebra and mathematical analysis, basic programming skills. Students will need to bring their own laptop.			
Assessment: Reports for laboratory assignments and final exam			